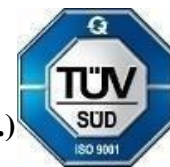




MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(Deemed to be University)

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2024 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH, CST), MBA & MCA



A Report on Alumni Guest Talk on
“VLSI and India’s Semiconductor Revolution: Careers, Growth, and Opportunities”
Organized by Department of Electronics & Communication Engineering
28.08.2025



Report Submitted by : Mr G Charan Kumar Assistant Professor & Alumni Coordinator Department of Electronics & Communication Engineering.

Resource Person Details: Mr. Koti Reddy, Scientist from SAMEER (Society for Applied Microwave Electronics Engineering and Research), MeitY, Government of India.

Attendees : 60 Students including Faculty.

Report Received on 10.09.2025

Mode of Conduct: Offline.

The Department of Electronics and Communication Engineering (ECE), in association with the Alumni Welfare Association of MITS Deemed to be University, organized an Alumni Guest Talk on “VLSI and India’s Semiconductor Revolution: Careers, Growth, and Opportunities” on 28th August 2025. The session was conducted in the Scale Up Room from 10:00 AM to 12:00 PM.



The distinguished **Resource Person** for the session was **Mr. Koti Reddy**, Scientist from **SAMEER (Society for Applied Microwave Electronics Engineering and Research)**, MeitY, Government of India.

The program commenced with a formal welcome address, followed by an insightful talk by the resource person. Mr. Koti Reddy highlighted the pivotal role of **VLSI design and semiconductor technology** in India’s emerging status as a global hub for electronics manufacturing. He elaborated on the government’s initiatives under **Make in India and Digital India missions**, the importance of semiconductor fabs, and the growing industry demand for skilled VLSI professionals.

He motivated students to explore career opportunities in **IC design, verification, chip testing, and fabrication technologies**, emphasizing the need for innovation, research, and industry collaborations. The interactive session enabled students to gain valuable knowledge about upcoming trends, career pathways, and the global relevance of India’s semiconductor ecosystem.



Program Outcomes

- Students gained insights into the current scenario and future scope of India's semiconductor industry.
- They understood the career opportunities and skill requirements in the field of VLSI design.
- The session encouraged students to align their academic learning with industry demands in semiconductor technology.
- It inspired participants to contribute to India's mission of becoming a global semiconductor leader.

The session was highly informative and inspiring, leaving the students motivated to explore and pursue careers in the ever-growing field of **VLSI and semiconductor technologies**.

The program concluded with a **Vote of Thanks** delivered by **Mr. G. Charan Kumar, Assistant Professor and Alumni Coordinator, Department of ECE**, who expressed gratitude to the **MITS Deemed to be University Management, Dr. C. Yuvaraj Sir Hon'ble Vice Chancellor, Dr. P. Ramanathan Principal, Dr. S. Rajasekaran HOD-ECE, Dr. R. Kiran Kumar, Alumni Relation Officer** for their continuous encouragement and support.

Special thanks mentioned for **Mr V Maruthi Prasad Senior Manager Communications, Dr. Naga Swetha Assistant Professor Department of ECE** who supported for Organising the Guest Lecture **Mr. Y. Pradeep Kumar Transport In charge** for ensuring smooth logistics

The session proved to be highly beneficial, inspiring students to pursue excellence in **VLSI and semiconductor careers** while contributing to India's technological advancement.

Felicitation for Resource Person:

